

**IN THE SPECIFICATION:**

Kindly amend page 4 lines 4 to 8, as follows:

Outputs 120 and 130 are generally configured to receive clock signals from drivers 140 and 150. In accordance with one exemplary embodiment of the present invention, outputs 120 and 130 are further configured to electrically couple to portions of a microprocessor 300. One technique for coupling device 100 to the microprocessor 300 includes use of C4 technology bumps. Thus, in accordance with one exemplary embodiment, outputs 120 and 130 include C4 bumps.